

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL. NUM 10085889	FILING DATE 02/28/2002	CLASS 257	SUBCLASS 254	GAU 2811	EXAMINER Dran
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2815

****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials	ATTORNEY DOCKET NO SC11814TP
TITLE : Method of forming a semiconductor device having an energy absorbing layer and structure thereof	

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheet Dwg.	Fig. Dwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED F R ISSUE	
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